

REMARKS

Reconsideration of this application, as amended, is respectfully requested. The following remarks are responsive to the Office Action mailed March 3, 2004 and the Advisory Action mailed August 31, 2004.

Claim Objections

In the Office Action mailed, March 3, 2004, Claim 18 is objected to because in line 3, the word “controller” should have been inserted after the word “module”. Accordingly, the word “controller” has been inserted in claim 18 as requested in the Office Action.

In the Advisory Action mailed, August 31, 2004, the Examiner stated that the previously proposed Amendment is non-compliant with form for amendments stated in 37 CFR 1.121 for failing to list claims 1-17. Accordingly, Applicant has listed claims 1-17 as being canceled in the present Preliminary Amendment.

Claim Rejections

In the Office Action mailed March 3, 2004, claims 18-21, 23, 24, 27, and 30 are rejected under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 4,045,781 (hereinafter “Levy”).

Claim 18 as presently amended claims interface circuitry within the memory controller that enables the system bus to interface with a number of different memories having a number of different electrical signal quality requirements from each other. Levy, on the other hand discloses memory modules 30-33 having memory control and

timing circuitry 42 that enables the bus 40 to be interfaced **only** to a plurality of magnetic core storage elements 44, 45 (see Col. 6, Lns. 44-47). Levy does not, however, disclose a memory controller in the memory modules 30-33 having interface circuitry to interface the bus 40 to any other memory type having any other signal quality requirements than the magnetic core storage elements discussed in column 6, lines 44-47.

Although Levy does discuss that the memory control and timing circuitry 42 can interface the bus 40 to stacks 44, 45 having different **sizes** (Col. 18 Lns. 28-55), Levy does not teach or suggest that the memory control and timing circuitry 42 can interface the bus 40 to varying types of memory having varying types of signal quality requirements, as in presently amended claim 18.

Accordingly, it is respectfully asserted by Applicant that neither presently-amended claim 18, nor any dependent claims of claim 18, are anticipated by Levy and is in condition for allowance.

Claims 22, 25, 26, 28 and 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 4,045,781 to Levy et al.

In view of the above amendments and arguments regarding claim 18, it is asserted by Applicant that limitations within claims 22, 25, 26, 28 and 29 are not taught or suggested by Levy, and are therefore not obvious in view of Levy. Accordingly, Applicant respectfully asserts that claims 22, 25, 26, 28 and 29 are in condition for allowance.

Claims 21-26 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 4,045,781 to Levy et al. in view of U.S. Patent 5,257,233 to Schaefer. In

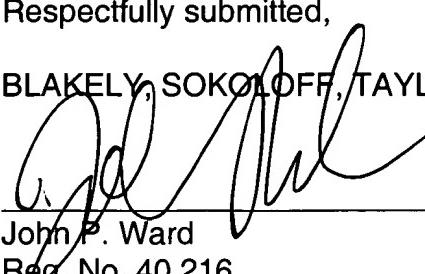
view of the above amendments and arguments regarding claim 18, it is asserted by Applicant that limitations within claims 21-26 are not taught or suggested by Levy nor Schaefer, and are therefore not unpatentable over Levy in view of Schaefer. Accordingly, Applicant respectfully asserts that claims 21-26 are in condition for allowance.

Claims 21-26 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 4,045,781 to Levy et al. in view of U.S. Patent 5,036,493 to Nielsen. In view of the above amendments and arguments regarding claim 18, it is asserted by Applicant that limitations within claims 21-26 are not taught or suggested by Levy nor Nielsen, and are therefore not unpatentable over Levy in view of Nielsen. Accordingly, Applicant respectfully asserts that claims 21-26 are in condition for allowance.

Please charge any fees not covered by any checks submitted herewith to our Deposit Account No. 02-2666.

Respectfully submitted,

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